



THE UNITED STATES PATENT AND TRADEMARK OFFICE

Appl. No. : 10/829,380 Confirmation No. 7082  
Applicant : A. HAYASHI et al  
Filed : April 22, 2004  
Title : A SEMICONDUCTOR INTEGRATED CIRCUIT  
CONTROLLING OUTPUT IMPEDANCE AND SLEW RATE  
TC/AU : 2816  
Examiner : T. Lam  
Docket No. : H-1139  
Customer No.: 24956

Commissioner for Patents  
P.O. Box 1450  
Alexandria, VA 22313-1450

**AMENDMENT**

Sir:

In response to the Office Action of August 25, 2006, please amend the above-identified application as follows. A Petition and fee for a three-month Extension of Time also accompanies this response.

**Amendments to the Claims** are reflected in the listing of claims which begins on page 2 of this paper.

**Remarks / Arguments** begin on page 13 of this paper.